



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-35430

Gerard Chauvel, et al.

Art Unit: 2183

Serial No: 10/631,252

Examiner:

Filed: July 31, 2003

Conf. No.: 1620

For: Methods and Apparatuses for Managing Memory

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

P.O Box 1450

Alexandria, VA 22313-1450

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Robin E. Bamum

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith. The relevancy of each of the references is noted on the enclosed Search Reports.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,

Robert D. Marshall, Jr.
Attorney for Applicant
Reg. No. 28,527

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5290

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Complete If Known

Sheet	1	of	1	Application Number	
				Filing Date	July 31, 2003
				First Named Inventor	Gerard Chauvel, et al.
				Group Art Unit	2183
				Examiner Name	
				Attorney Docket No.	TI-35430

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document	Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

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	CA	L. Lopriore; <i>Line Fetch/Prefetch in a Stack Cache Memory, Microprocessors and Microsystems</i> , IPC Business Press Ltd., London, GB, vol. 17, no. 9, 1 Nov. 1993, pgs. 547-555.	
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